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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/765,637	
	Filing Date	1-27-04	
	First Named Inventor	ADOLPH MONDRY	
	Art Unit	3743	
	Examiner Name	JORGE PEREIRO	
Total Number of Pages in This Submission	3	Attorney Docket Number	NONE

ENCLOSURES (Check all that apply)		
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Firm or Individual name	ADOLPH MONDRY
Signature	Adolph Mondry
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Application Control Number 10/765,637

Method for automatically maintaining a multiburner furnace

Art Unit 3743

Examiner Jorge Pereiro

Request for Continuing Examination

(RCE)

Title of the Invention -

Method for

automatically maintaining

a multiburner furnace.

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In response to the Advisory Action Before Filing of an Appeal Brief the following request for Continuing Examination (RCE) in compliance with 37CRF1.114 follows.

In the office action summary by examiner Andrew St Clair Claims 1-20 were rejected under 35USC102 and 35USC112. All comments of the examiner were then employed as amended claims and other amended sections of the present patent application to render all parts of the patent application more definite and clear by 35USC112.

The method of Al-Halbouni differs significantly from the method of the present invention in that the method of Al-Halbouni does not utilize the smallest and largest oxidant dosage as defined in the present method to intersect the desired flue parameter yielding the selected oxidant flow rate to the multiburners of the furnace. This is demonstrated in the Cartesian plane and in the logic flow charts. Thus, under 35USC102(b) the basis for rejection is mute.

No new issues are raised. All corrections are made for clarity to satisfy the examiner's comments.

The multiburner furnace minimum oxidant dosage curve is properly traced. It is described by the function $y=1/x$, instead of $y=\log$ to the base b of x as erroneously stated previously and now stands corrected. The flow charts verify this fact. Since measured values are exclusively employed, all functions listed are employed for conceptual purposes in the design of the logic flow charts.